

ASICone

Questions

Who of you
did use Yosys in the past?

Questions

Who did a tapeout?

Questions

Who did a tapeout
only using open source tools?

Questions

Who want to do a tapeout?

What is ASICone

- Success is real silicon, not a published paper!
- RISC-V testchip, similar to Raven, QFN48
- learn, improve, test
- cooperate
- define and document tools and process
- no commercial goal
- sponsored by Symbiotic EDA

Symbiotic EDA

- Clifford Wolf of Yosys and icestorm fame
- Edmund Humenberger of no fame
- distributed team of 10 engineers
- self funded & profitable
- Formal Verification tools & FPGA tools
- Founded Feb 2018
- company strategy: hard problems & no BS

How

- Mainly open source tools
 - PolytEDA for physical verification
- still evaluating
- small qualified team, working fulltime
- MVP, KISS
- Porting analog blocks from TSMC 180

Where

- XFab XH018 6-metal
- Use PDK under NDA

When

- Cell-Lib and analog test tapeout August 2019
- MPW tapeout Nov 2019 10-25mm²

Who

- Niels (ASIC tools)
- Elkim (chip design, analog IP)
- Matt Gutthaus (OpenRAM)
- Ckristian Duran (digital)
- Luis E. Rueda G. (analog)
- Tim Edwards (advisor)

Cooperation with Jeff Carr

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Q&A

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